



# TSC Features (1/2) 88

- Proven and robust surface charge transfer acquisition principle available on **STM32F0**, **STM32F3**, **STM32L0** and **STM32L4** series
- Supports up to 24 capacitive sensing channels split over 8 analog I/O groups
  - Number of channels and analog I/O groups depend on the device used
- Up to 8 capacitive sensing channels can be acquired in parallel offering a very good response time
  - 1 counter per analog I/O group to store the current acquisition result
- One sampling capacitor for up to 3 capacitive sensing channels to reduce the system components
- Full hardware management of the charge transfer acquisition sequence
  - No CPU load during acquisition
- Spread spectrum to improve system robustness in noisy environment



## TSC Features (2/2) 89

- Programmable charge transfer frequency
- Programmable sampling capacitor I/O pin and channel I/O pin
  - Any GPIO of an analog IO group can be used for the sampling capacitor
  - Any GPIO of an analog IO group can be used for the channel
- SYNC input pin to synchronize the acquisition with an external signal
- **Programmable max count value** to avoid long acquisition when a channel is faulty
- Dedicated end of acquisition and max count error flags with interrupt capability
- Compatible with proximity, touchkey, linear and rotary touch sensors
- Designed to operate with **STMTouch touch sensing middleware** available in corresponding STM32Cube package



#### STM32F051x TSC Overview 90

- Supports up to **18** capacitive sensing channels split over **6** analog I/O groups
- 6.8 MHz maximum charge transfer frequency

	Number of capacitive sensing channels			
Analog I/O group	STM32F051Rx	STM32F051Cx	STM32F051Kx	
G1	3	3	3	
G2	3	3	3 2	
G3	3	2		
G4	3	3	3	
G5	3	3	3	
G6	3	3	0	
Number of capacitive sensing channels	18	17	14	



#### STM32F302/303 TSC Overview 91

- Supports up to 24 capacitive sensing channels split over 8 analog I/O groups
- 10.2 MHz maximum charge transfer frequency

Analog I/O group	Number of capacitive sensing channels			
Analog I/O group	STM32F30xVx	STM32F30xRx	STM32F30xCx	
G1	3	3	3	
G2	3	3	3 2 3 3 3 0	
G3	3	3		
G4	3	3		
G5	3	3		
G6	3	3		
G7	3	0		
G8	3	0	0	
Number of capacitive sensing channels	24	18	17	



#### STM32F372/373 TSC Overview 92

- Supports up to 24 capacitive sensing channels split over 8 analog I/O groups
- 10.2 MHz maximum charge transfer frequency

	Number of capacitive sensing channels			
Analog I/O group	STM32F37xVx	STM32F37xRx	STM32F37xCx	
G1	3	3	3	
G2	3	3	2 1 3 3 2 0 0	
G3	3	3		
G4	3	3		
G5	3	3		
G6	3	2		
G7	3	0		
G8	3	0		
Number of capacitive sensing channels	24	17	14	



## STM32L4 TSC Overview 93

- Supports up to 24 capacitive sensing channels split over 8 analog I/O groups
- 16 MHz maximum charge transfer frequency

	Number of capacitive sensing channels			
Analog I/O group	STM32L486Zx STM32L486Qx STM32L476Zx STM32L476Qx STM32L471Zx STM32L471Qx	STM32L486Vx STM32L476Vx STM32L471Vx	STM32L486Jx STM32L486Rx STM32L476Jx STM32L476Rx STM32L471Jx STM32L471Rx	
G1	3	3	3	
G2	3	3	3	
G3	3	3	0 3	
G4	3	3		
G5	3	3	0	
G6	3	3	0	
G7	3	3	3	
G8	3	0	0	
Number of capacitive sensing channels	24	21	12	



#### TSC Block Diagram 94





#### Charge Transfer Measuring Circuit 55

- Rs is used to improve ESD robustness (typically 1K)
- Cs sampling capacitor value depends on the required channels sensitivity
  - Higher Cs value is, higher the sensitivity but longer the acquisition time is



#### Charge Transfer Acquisition Overview

- Charge transfer uses the electrical properties of the capacitor charge Q
- It uses a sampling capacitor (C<sub>s</sub>) in which the electrode (C<sub>x</sub>) charges are transferred to
- Charge Transfer is performed through analog switches directly embedded into the GPIO
- The charge transfer cycle is repeated N times until the voltage on the sampling capacitor reaches the V<sub>IH</sub> threshold of the GPIO it is connected to
- The number N of transfer cycles required to reach the threshold represents the size of Cx
  - The number of transfer decreases when the electrode is touched.



#### Charge Transfer Acquisition Sequence



life, augment

S4 closed for the whole acquisition S5 & S6 opened for the whole acquisition

	Step	<b>S</b> 3	<b>S</b> 2	<b>S1</b>	Description		
t until Vcs as a '1'	1	Closed	Opened	Closed	Cs discharge		
	2	Opened	Opened	Opened	Deadtime		
	3	Opened	Closed	Opened	Charge cycle (Cx charge)		
	4	Opened	Opened	Opened	Deadtime		
	5	Opened	Opened	Closed	Transfer cycle (charge transferred to Cs)		
	6	Opened	Opened	Opened	Deadtime		
	<b>7</b> Presentati	Closed	Opened	Closed	Cx discharge		

#### GPIO Analog Switch and Hysteresis Control 98

- In addition to the management of charge transfer acquisition, the touch sensing controller provides a manual control for both the embedded analog switch and hysteresis of the GPIOs belonging to the analog I/O groups.
- This could be useful to implement a different capacitive sensing acquisition principle of for others purpose (ie: analog multiplexor).



#### STM32 Touch Sensing Library 99

- Complete free C source code library with firmware application examples
  - Available in corresponding STM32Cube packages
- Support of proximity, touchkey, linear and rotary touch sensors
- **Multifunction capability** to combine capacitive sensing functions with traditional MCU features
- Enhanced processing features for optimized sensitivity and immunity
  - Calibration, environment control system (ECS), debounce filtering , detection exclusion system (DxS), ...
- Complete and simple API for status reporting and application configuration
- Compliant with MISRA
- Compliant with all STM32 C compilers







# TSC Features (1/2) 101

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